

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. 10/264,615
Priority Filing Date October 3, 2002
Inventor Luan C. Tran
Assignee Micron Technology, Inc.
Priority Group Art Unit 2813
Priority Examiner Laura M. Schillinger
Attorney's Docket No. MI22-2331
Title: Semiconductor Processing Methods of Forming Integrated Circuitry

Commissioner for Patents
Attention: Official Draftsman
P.O. Box 1450
Alexandria, VA 22313-1450

SUBMISSION OF FORMAL DRAWINGS

Please enter the enclosed formal drawings in the above-referenced application in place of drawings originally filed.

Acknowledgment of receipt of the formal drawings and their acceptance into the file is requested.

Respectfully submitted,

Dated: July 11, 2003

By: 
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Enclosures: 4 Sheets of Formal Drawings, Figs. 1-7.